DAFTAR PUSTAKA

[1] Beck, Kent. "Extreme Programming". www.extremeprogramming.org. diakses pada tanggal 22 Juni 2011, 12:22 WIB.

[2] Hendrawati, Retno IR, MT. & Hariyanto, Bambang. 2000. Logika Matematika. Bandung: Informatika Bandung.

[3] Munir, Rinaldi. 2005. *Matematika Diskrit*. Bandung: Informatika Bandung

[4] Pengantar Logika Matematika. 2004. Bandung: Informatika Bandung.

[5] Pramono, Djoko. 2002. Mudah menguasai *Visual Basic 6*. Jakarta: PT. Elex Media Komputindo.

[6] Pressman, Roger.S. 1997. "Software Engineering : A Practioner's Approach. "4th. McGrawHill.

[7] Prof. Dr. Sugiyono. 2010. Metode Penelitian Kuantitatif Kualitatif Dan R&D. Bandung: Alfabeta.

[8] Rosa A.S, M. Shalahuddin, 2011. *Modul Pembelajaran Rekayasa Perangkat Lunak (trstruktur dan Berorientasi Objek)*. Bandung: Modula.

[9] Samuel C, Lee. Sutisna. 1994. Rangkaian Digitan dan Rangkaian Logika (*Digital circuits & Logic design*). Jakarta: Erlangga.

[10] Sarwono, Jonathan. 2006. *Metode Penelitian Kuantitatif dan Kualitatif*. Yogyakarta: Graha Ilmu.

[11] Sommerville, Ian. 2001. "Software Engineering" 6th. Addison Wesley.

[12] Sommerville, Ian. 2003. *Software Enginering (Rekayasa Perangkat Lunak) edisi 6 jilid 1*. Jakarta: Erlangga.

[13] Sugiyono. 2010. *Metode Penelitian Kuantitatif,, Kualitatif dan R & D.*Bandung: Alfabeta

[14] Suryokusumo, Ario. 2001. *Microsoft Visual Basic 6.0*. Jakarta: PT. Elex Media Komputindo.

[15] Quine, W. V. 1952.”*The Problem of Simplifying Truth Function*”. USA: American Mathematical Monthly.

[16] McCluskey, E. J. 1956. “*Algebraic Minimization and the Design of Two Terminal Contact Networks*”. USA: Bell System Technical Journal.

[17] Caldwell, S. M. “*Switching Circuits and Logical Design*”. 1958. New York: John Wiley & Sons.Inc.

[18] Mersereau, W. A. 1965. ”*Reduction of Switching Circuits Algebra to Prime Implicants by a Digital Computer*”. Senior Thesis University of New Brunswick.